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First Named Inventor	Chong, Tee
Group Art Unit	2827
Examiner Name	Vigushin, John

Sheet 1 of 1

Attorney Docket No: 884.419US1

**US PATENT DOCUMENTS**

Examiner Initial*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
JV	US-4,495,377	01/22/1985	Johnson, C. L., et al.	474.36	68.5 777	12/30/1982
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
JV	EP-0883182	12/09/1998	Horiuchi, M., et al.	H01L	23/523	
JV	EP-0921567	06/09/1999	Horiuchi, M., et al.	H01L	23/498	
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JV	EP-1071316	01/24/2001	Horiuchi, M., et al.	H05K	1/18	
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>3</sup>
JV		DEHKORDI, P., et al., "Determination of Area-Array Bond Pitch for Optimum MCM Systems: A Case Study", <u>Proceedings of the IEEE Multi-Chip Module Conference, Los Alamitos, (Feb. 4, 1997), 8-12.</u>	
JV		FJELSTAD, J., "Chip Scale Packages -- Their Future Impact on PCB Design", <u>Electronic Engineering, (Mar., 1998), 75-79.</u>	

EXAMINER

*John B. Vigushin*

DATE CONSIDERED

*June 12, 2003*

\* EXAMINER: Insert if reference considered, whether or not citation is in conformance with MPEP 909. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached.

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